

SRAM 200 operates as follows. In response to an externally applied column address signal, a column decoder, not shown, drives column selector CS to connect bit line pair BL and BL and sense amplifier SA30 for a corresponding column. A constant bias current $I_{sub.B}$ limited by bit line load BR flows through each of bit lines BL and BL connected to sense amplifier SA30. In response to an externally applied row address signal, row decoder RD

